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APPLICANTS

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** CONTINUING DATA ***** *BT*

** FOREIGN APPLICATIONS ***** *BT*
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 2
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Examiner's Signature *[Signature]* Initials *MS*

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TITLE
 Method of designing semiconductor integrated circuit with accurate capacitance extraction

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